



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the application of: Daniel Ortega Gonzalez *et al.*

Group Art Unit: 2874

Serial No.: 09/870,535

Examiner:

Filed: May 30, 2001

For: *ARRAYED WAVEGUIDE GRATING WITH
REDUCED CROSSTALK*

Attorney Docket No.: KLW-001

Commissioner for Patents
Washington, D.C. 20231

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By:

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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 CFR §1.56, §1.97, and §1.98, Applicants herewith make of record the following publications which may be material during the prosecution of the above-referenced patent application.

EP 301 194 B1, published September 15, 1993;

WO 98/58283 A2, published December 23, 1998;

Japanese Patent Publication 11-133253 A, published May 21, 1999;

Japanese Patent Publication 00-171648 A, published June 23, 2000;

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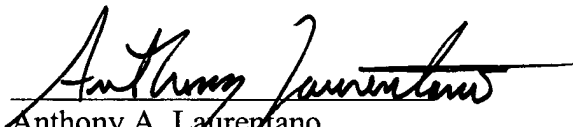
Inoue, Y. et al. "Silica-based arrayed-waveguide grating circuit as optical splitter/router" *Electronics Letters* 31(9):726-727 (27 Apr 1995);

Okayama, H. and Kawahara, M. "Prototype 32x32 optical switch matrix" *Electronics Letters* 30(14):1128-1129 (7 Jul 1994).

This statement is *not* to be interpreted as a representation that the cited publications are material, that the Applicants have conducted an exhaustive search, or that no other relevant information exists. Nor shall the citation of any publication herein be construed *per se* as a representation that such publication is prior art. Moreover, Applicants understand that the Examiner will make an independent evaluation of the cited publications.

Under 37 CFR § 1.97(b)(3), no additional costs are believed to be due in connection with the filing of this disclosure. If, however, a first Office Action on the merits issues in this application bearing a mailing date prior to the date of this Information Disclosure Statement, please charge the appropriate fee as required under 37 CFR §1.17(p) to our Deposit Order Account No. 12-0080.

Respectfully submitted,
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